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·	Application No.	Applicant(s)	
A1	10/695,478	IDOTA ET AL.	
Notice of Allowability	Examiner	Art Unit	
	Anh D. Mai	2814	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.31	(OR REMAINS) CLOSED () or other appropriate comm RIGHTS. This application is	in this application. If not include nunication will be mailed in due	ed course. THIS
1. \boxtimes This communication is responsive to <u>the Response filed N</u>	<u> 1, 2005</u>		
2. The allowed claim(s) is/are 1-6,8 and 9.	•		
3. The drawings filed on 29 October 2003 are accepted by the	ne Examiner.		
 4. Acknowledgment is made of a claim for foreign priority u a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have the certified copies of the priority documents. 	e been received. e been received in Applicati	on No	tion from the
* Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		e a reply complying with the red	quirements
5. A SUBSTITUTE OATH OR DECLARATION must be subminformal PATENT APPLICATION (PTO-152) which give			OTICE OF
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") mu (a) ☐ including changes required by the Notice of Draftsper 1) ☐ hereto or 2) ☐ to Paper No./Mail Date (b) ☐ including changes required by the attached Examiner Paper No./Mail Date	son's Patent Drawing Revie -		
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	1.84(c)) should be written on t the header according to 37 C	the drawings in the front (not the FR 1.121(d).	back) of
7. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT			Note the
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. 🔲 Interview S	nformal Patent Application (PTC Summary (PTO-413),	D-152)
3. ⊠ Information Disclosure Statements (PTO-1449 or PTO/SB/	Paper No 08), 7. ⊠ Examiner's	./Mail Date s Amendment/Comment	
Paper No./Mail Date <u>10/29/03</u> 4. ☐ Examiner's Comment Regarding Requirement for Deposit	8 ⊠ Evaminor's	s Statement of Reasons for Allo	wance
of Biological Material	9. ☐ Other		wance
o. Diological Material	9. [] Otilei	 ·	

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Election/Restrictions

1. Applicant's election without traverse of Group I, claims 1-6, 8 and 9 in the reply filed on March 1, 2005 is acknowledged.

2. This application is in condition for allowance except for the presence of claims 7, 10 and 11 to invention non-elected without traverse. Accordingly, claims 7, 10 and 11 have been cancelled.

EXAMINER'S AMENDMENT

3. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

In the Specifications:

New Title:

METHOD OF FABRICATING A BIPOLAR TRANSISTOR UTILIZING A DRY ETCHING AND A WET ETCHING TO DEFINE A BASE JUNCTION OPENING

In the Claims:

Cancel claims 7, 10 and 11.

Allowable Subject Matter

4. Claims 1-6, 8 and 9 are allowed.

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5. The following is an examiner's statement of reasons for allowance: prior art of record fails to teach a method for fabricating a semiconductor device in the combination of limitations as claimed including: a step (d) of forming, on the semiconductor substrate, an etching stopper layer which serves as an etching stopper against dry etching and which has insulating ability and a masking layer against wet etching in such a way that the masking layer overlies the etching stopper layer, a step (e) of exposing a part of the etching stopper layer by removing a part of the masking layer by means of dry etching; a step (f) of forming a base junction opening portion reaching the semiconductor layer by subjecting the part of the etching stopper layer exposed by means of dry etching of step (e) to a wet etching treatment using the remaining part of the masking layer as a mask.

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The prior art, in the formation of the base junction opening, teaches: forming a single etch mask layer on the semiconductor layer and performing a dry etch to form the opening.

- 6. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."
- 7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Anh D. Mai whose telephone number is (571) 272-1710. The examiner can normally be reached on 9:00AM-5:00PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

March 15, 2005